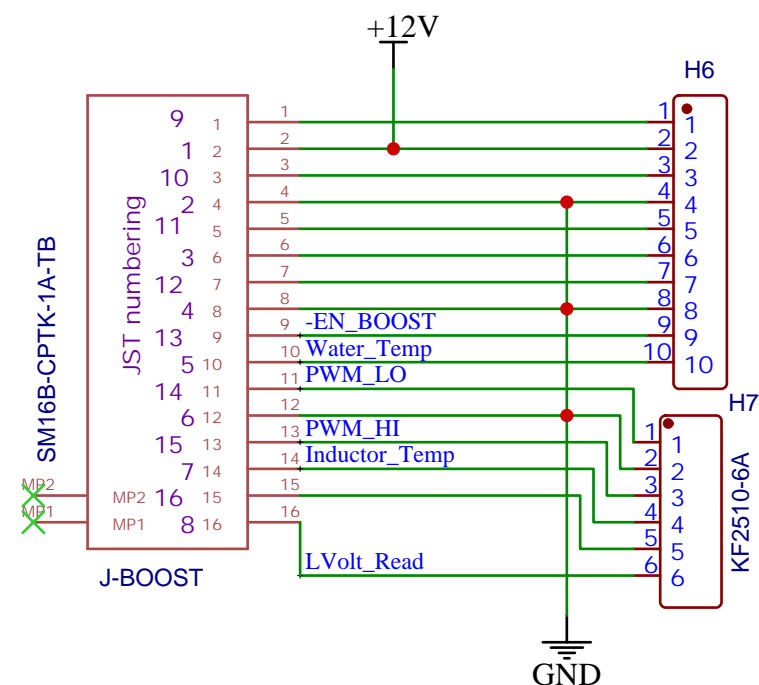


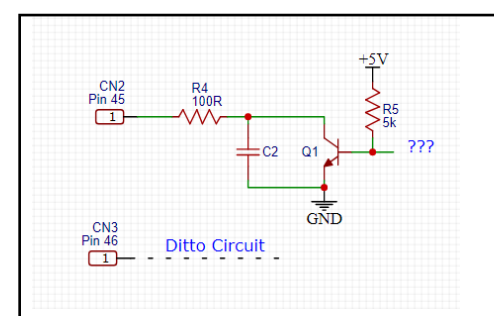
Unknowns	R - Gnd
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
30	9.8k
32	9.8k
43	20k
45	* 21.7k
46	

?VHU = Bus Voltage ?

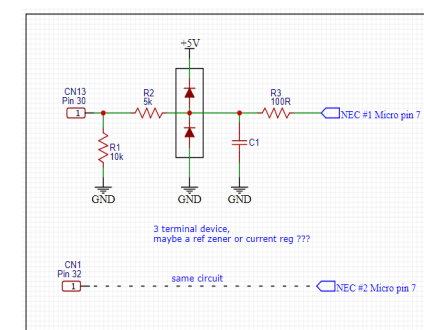


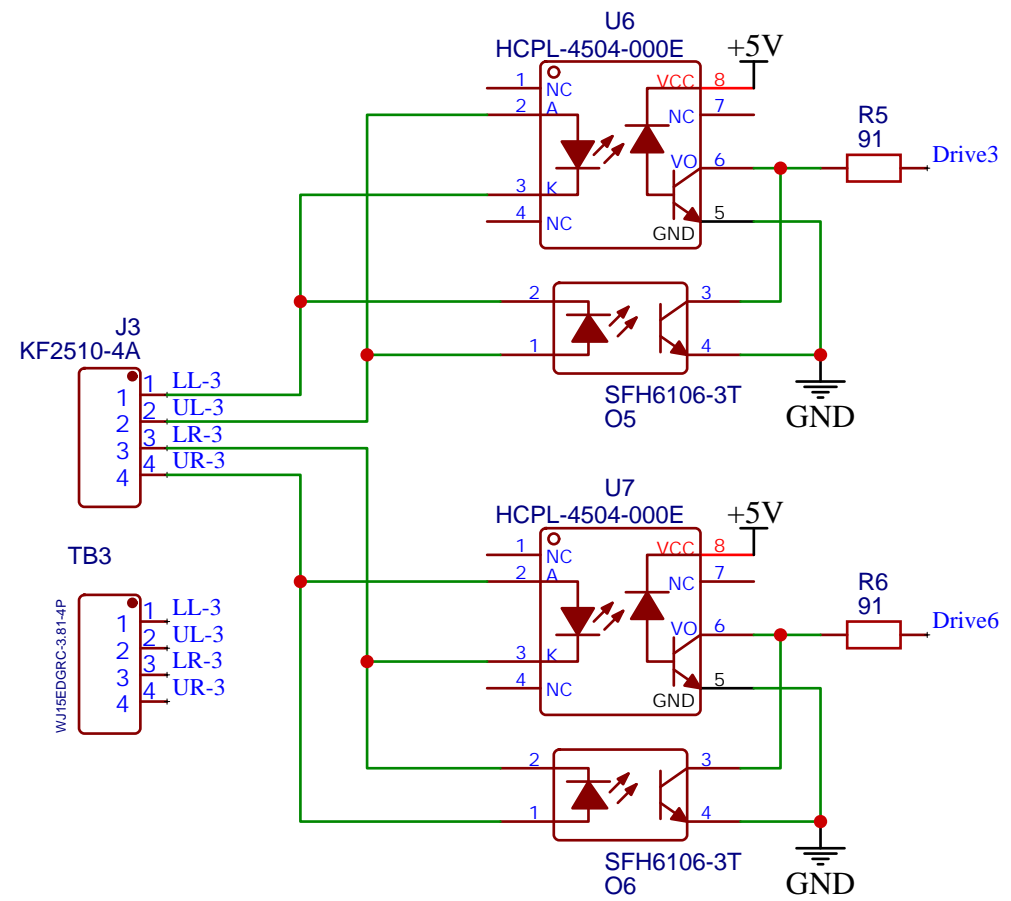
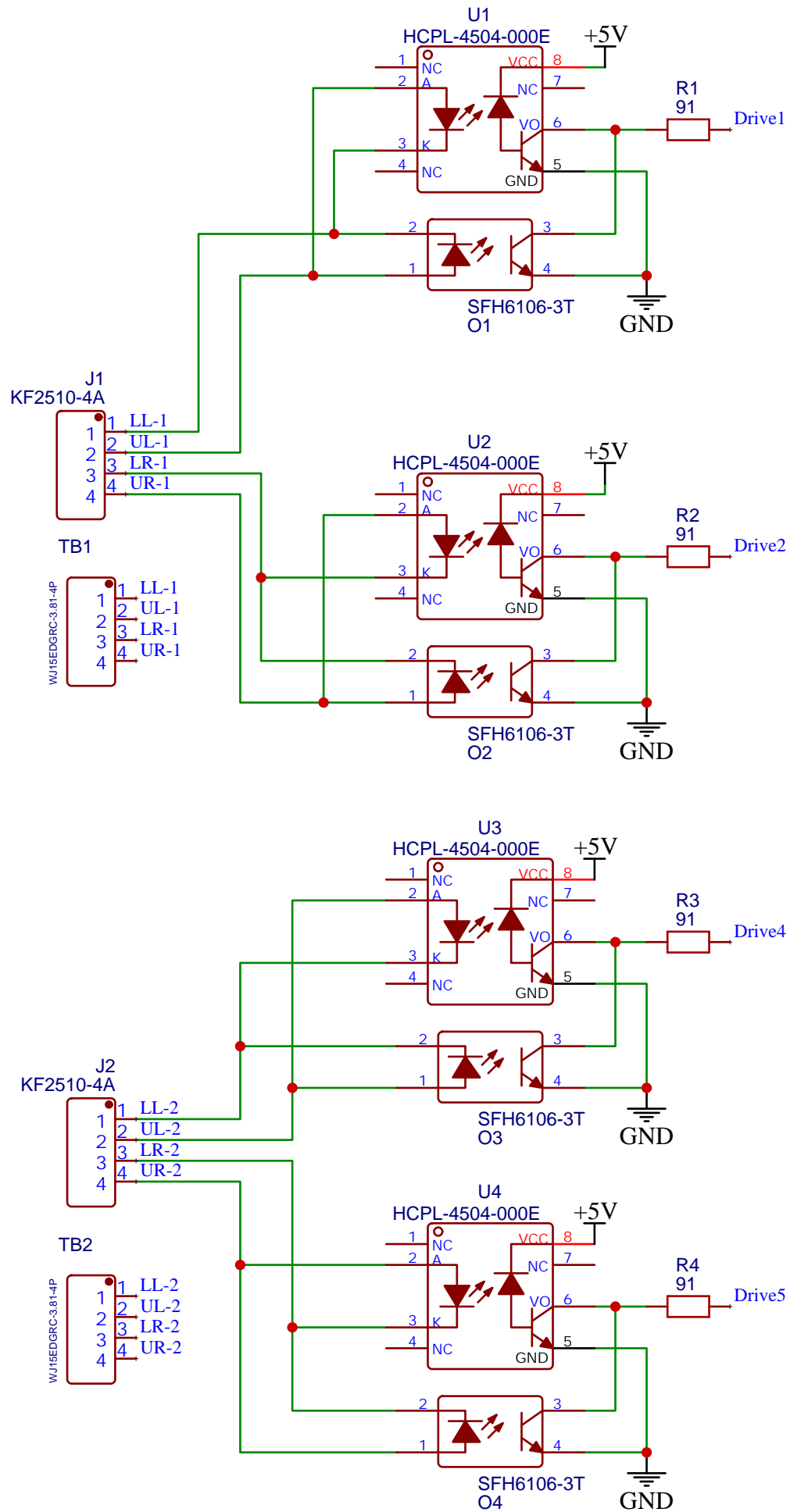
Connector Pins not laid out to JST spec

Pins 45, 46

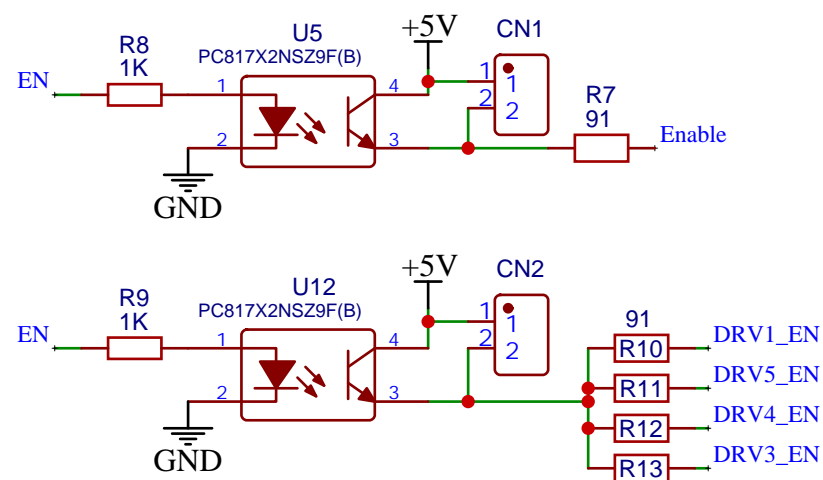
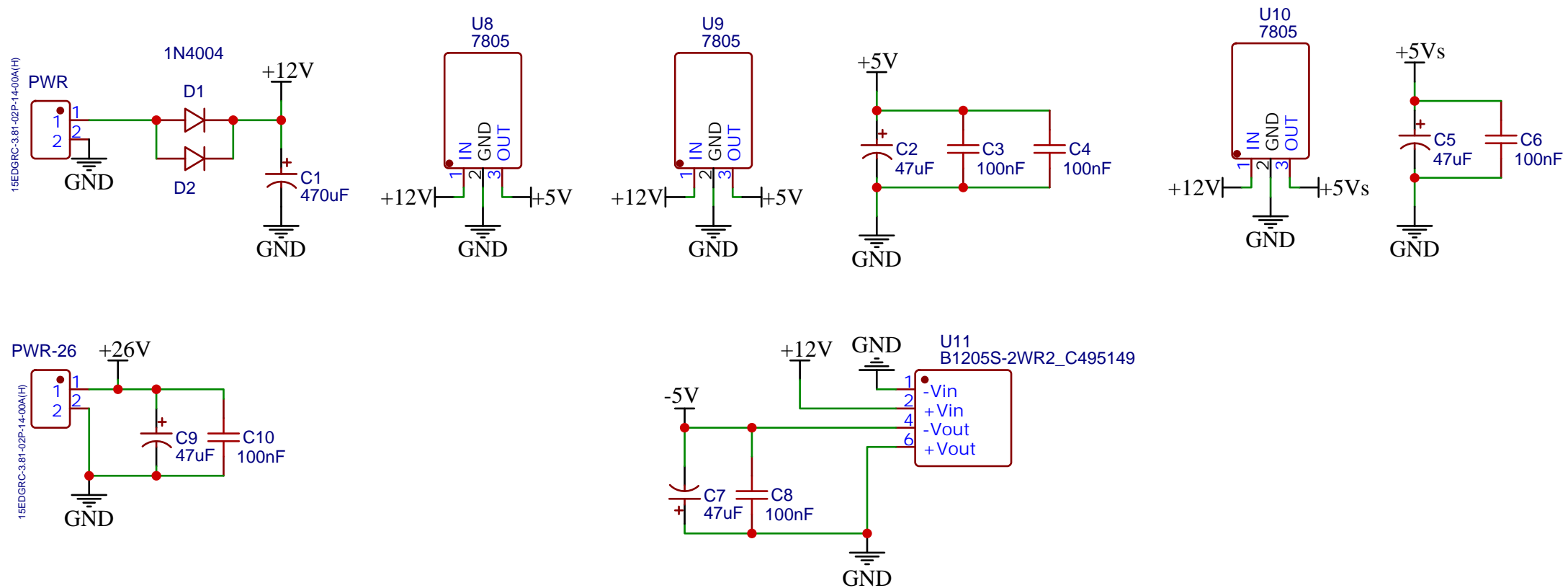


Pins 30, 32





TITLE: Drive Interface		REV: 1.0
	Company: Your Company	Sheet: 1/1
	Date: 2020-10-05 Drawn By: mackelec	



TITLE: Sheet_1		REV: 1.0
EasyEDA	Company: Your Company	Sheet: 1/1
	Date: 2020-10-12	Drawn By: mackelec