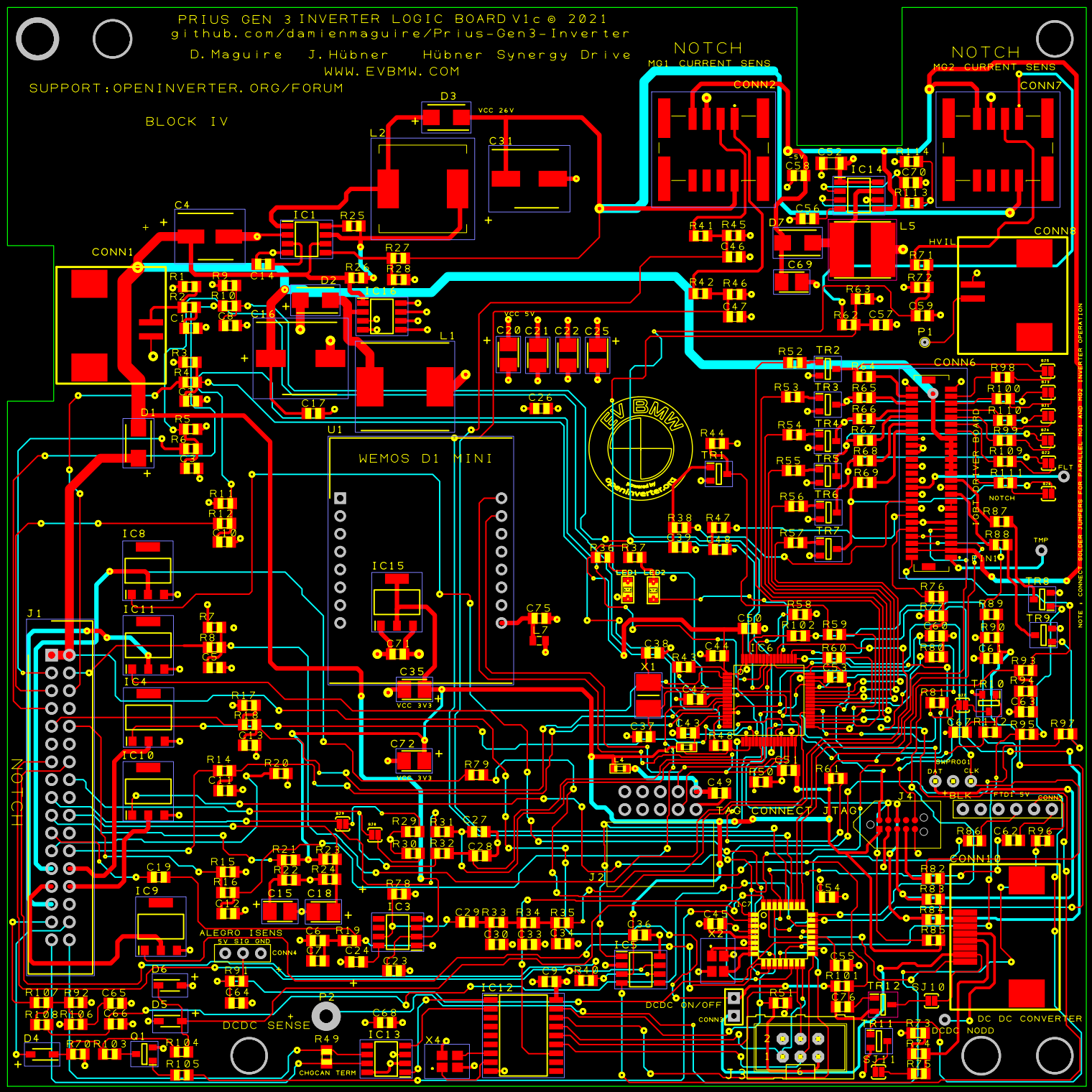
PRIUS GEN 3 INVERTER LOGIC BOARD V1c © 2021
github.com/damienmaguire/Prius-Gen3-Inverter
D. Maguire J. Hübner Hübner Synergy Drive
WWW.EVBMW.COM
SUPPORT:OPENINVERTER.ORG/FORUM
BLOCK IV
NOTCH
MG1 CURRENT SENS
CONN2
NOTCH
MG2 CURRENT SENS
CONN7
CONN1
CONN8
HVIL
CONN6
WEMOS D1 MINI
U1
IC15
IC1
IC2
IC3
IC4
IC5
IC6
IC8
IC9
IC10
IC11
IC12
IC13
IC14
L1
L2
L5
D1
D2
D3
D4
D5
D6
D7
TR1
TR2
TR3
TR4
TR5
TR6
TR7
TR8
TR9
TR10
TR11
TR12
X1
X2
X4
J1
J2
J3
J4
P1
P2
TAG CONNECT JTAG
SWPROG
DAT
CLK
BLK
FTDI 5V
CONN9
CONN10
ALEGRO ISENS
5V SIG GND
CONN4
DCDC SENSE
DCDC ON/OFF
CONN3
CHOCAN TERM
DC DC CONVERTER
DCDC NODD
SJ10
SJ11
LED1
LED2
FLT
TMP
NOTCH
NOTE 1 CONNECT SOLDER JUMPERS FOR PARALLEL HV1 AND HV2 INVERTER OPERATION